

# AdaWorld x 32-Bit Intel

- Highly Optimized Validated Ada Compiler
- Supports Protected Mode Applications
- Certification Package For Safety-Critical Applications
- Choice of Scheduling Policies
- Comprehensive Development Environment
- Fast and Small Real-Time Kernel
- Multi-Library System
- Extensive Cross Debugging Capability
- ICE Integration

The AdaWorld 32-bit Intel cross-development environments help to realize the full potential of Ada on embedded systems built with 32-bit Intel 80x86 processors. AdaWorld supports the development of 32-bit protected mode applications for all Intel386™, Intel486™, and Pentium® processors.

The highly optimized compiler has been fielded for well over a decade, and includes runtime configurations scalable from a minimal kernel (with a footprint of about 1.5K) to a full Ada executive

including multi-tasking and exception handling capabilities without arbitrary limitations. For complex applications that require reliable, predictable behavior, the real-time kernel supports Rate Monotonic Scheduling through the Priority Queuing and Priority Inheritance Protocol. When used with RMA tools, these scheduling policies provide a mathematical guarantee of tasking performance under even worst-case conditions.

The C-SMART certification package is also available. This package is essential for producing certified solutions to safety-critical applications, and is compliant with the most stringent FAA standards. It includes a complete set of supporting materials for achieving certification.

**HOST SYSTEMS SUPPORTED:**  
DOS, VAX/VMS, SPARC/Solaris

**PROCESSORS SUPPORTED:**  
Intel386 CX/EX/SX/SXSA/DX, Intel486 SX, IntelDX2™, IntelDX4™ and Pentium Processors

**AVAILABILITY:**  
Now

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